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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

None *Q*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-319849 11/01/2002

OK *Q*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>Shinobu Isobe</i> Initials <i>Q</i>	JAPAN	7	73	42

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## TITLE

Semiconductor device, and design method, inspection method, and design program therefor

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